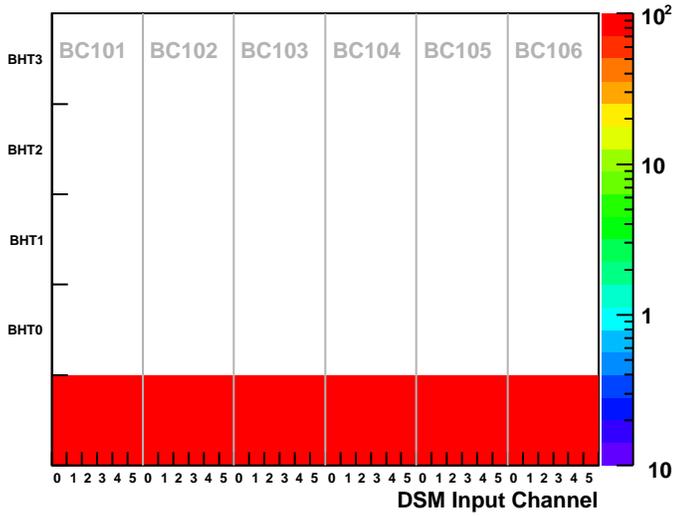


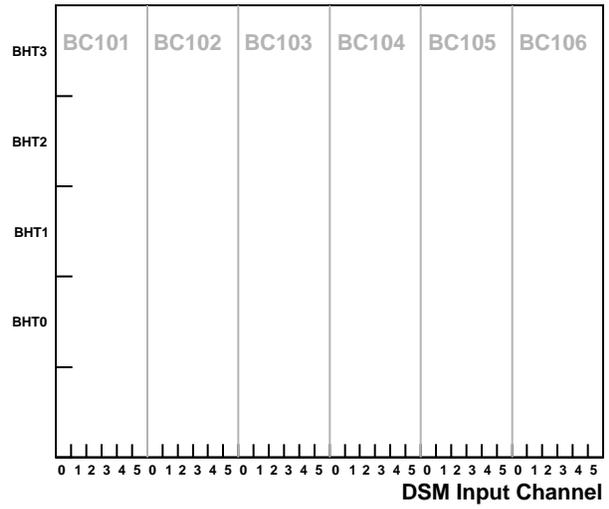
Barrel EMC L1 Input - High Tower Bits

Entries 3600



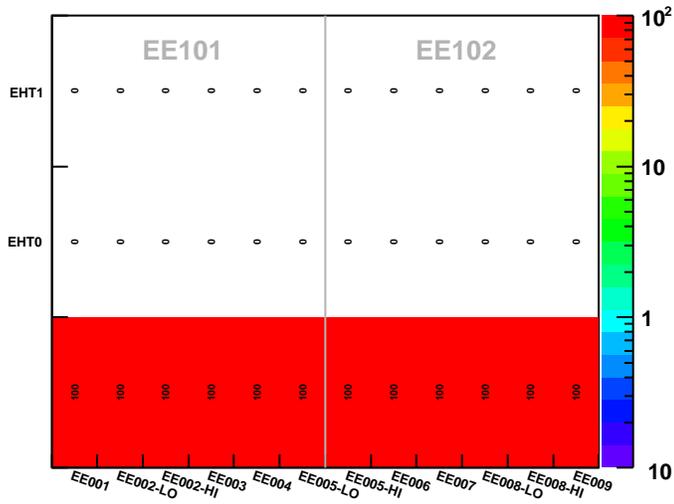
Barrel EMC L1 Input - High Tower Bits Errors

Entries 0



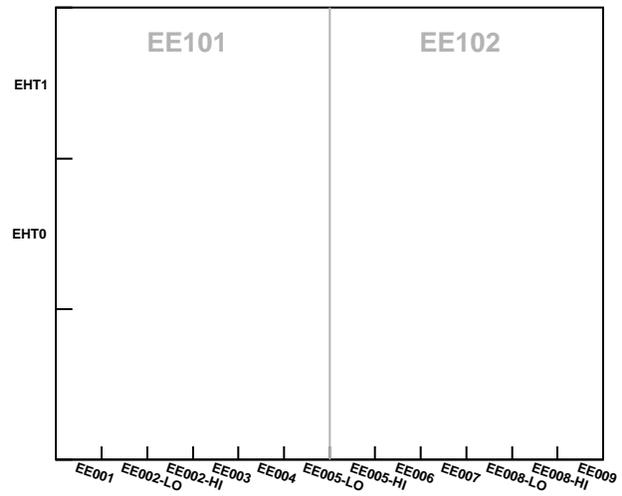
Endcap EMC L1 Input - High Tower Bits

Entries 1200



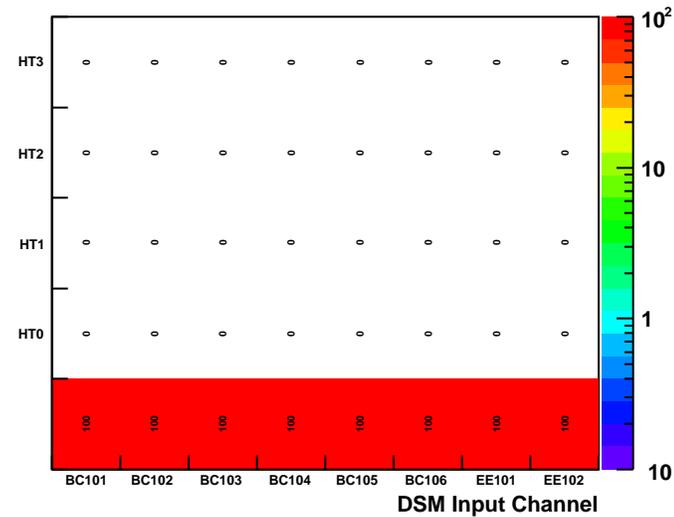
Endcap EMC L1 Input - High Tower Bits Errors

Entries 0



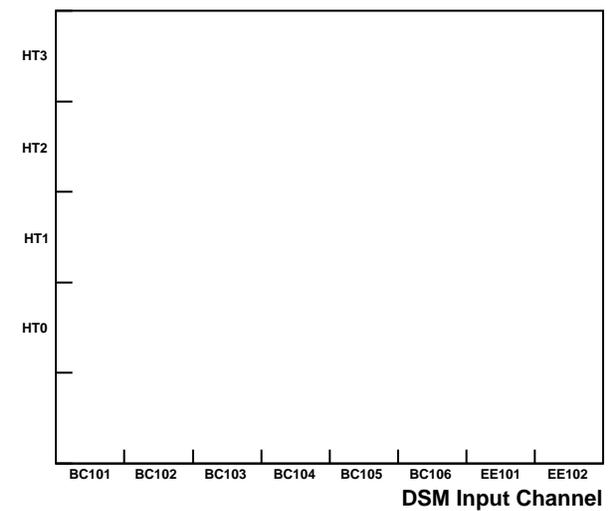
EMC L2 Input - High Tower Bits

Entries 800



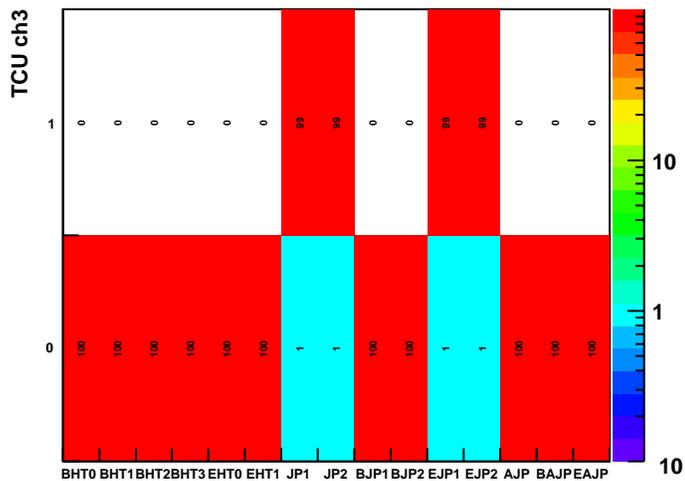
EMC L2 Input - High Tower Bits Errors

Entries 0



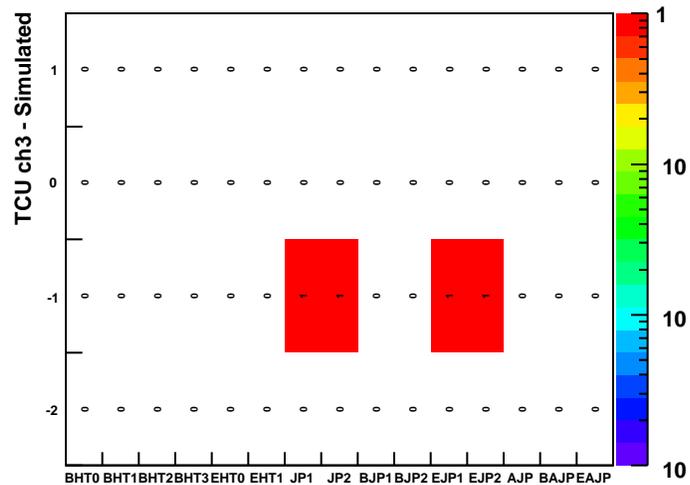
TCU ch3 Input (EM201)

Entries 1500



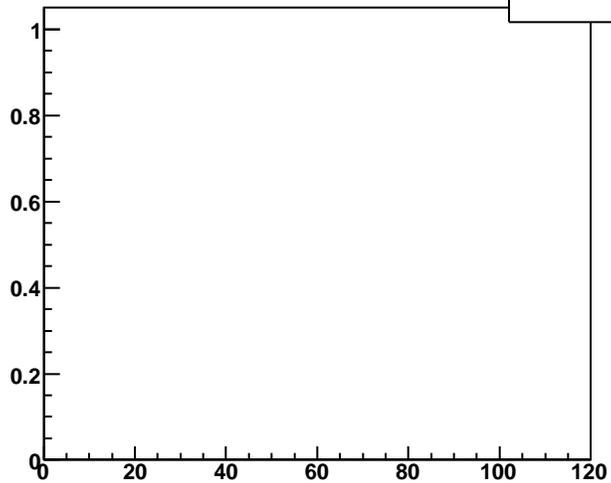
TCU ch3 Input (EM201)

Entries 4



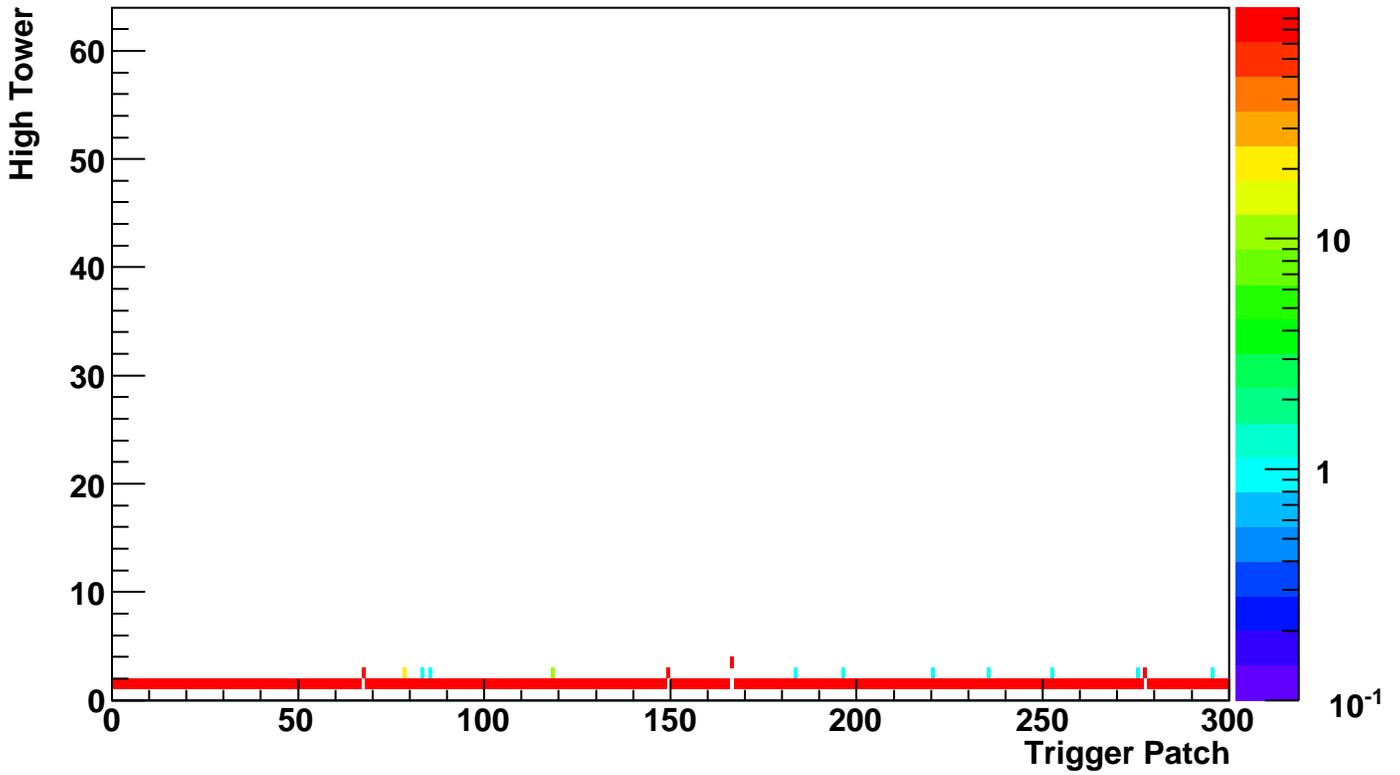
BunchId7Bit (BHT3)

Entries 0



Barrel EMC L0 Input - High Tower

Entries 30000



Endcap EMC L0 Input - High Tower

Entries 9000

